

D

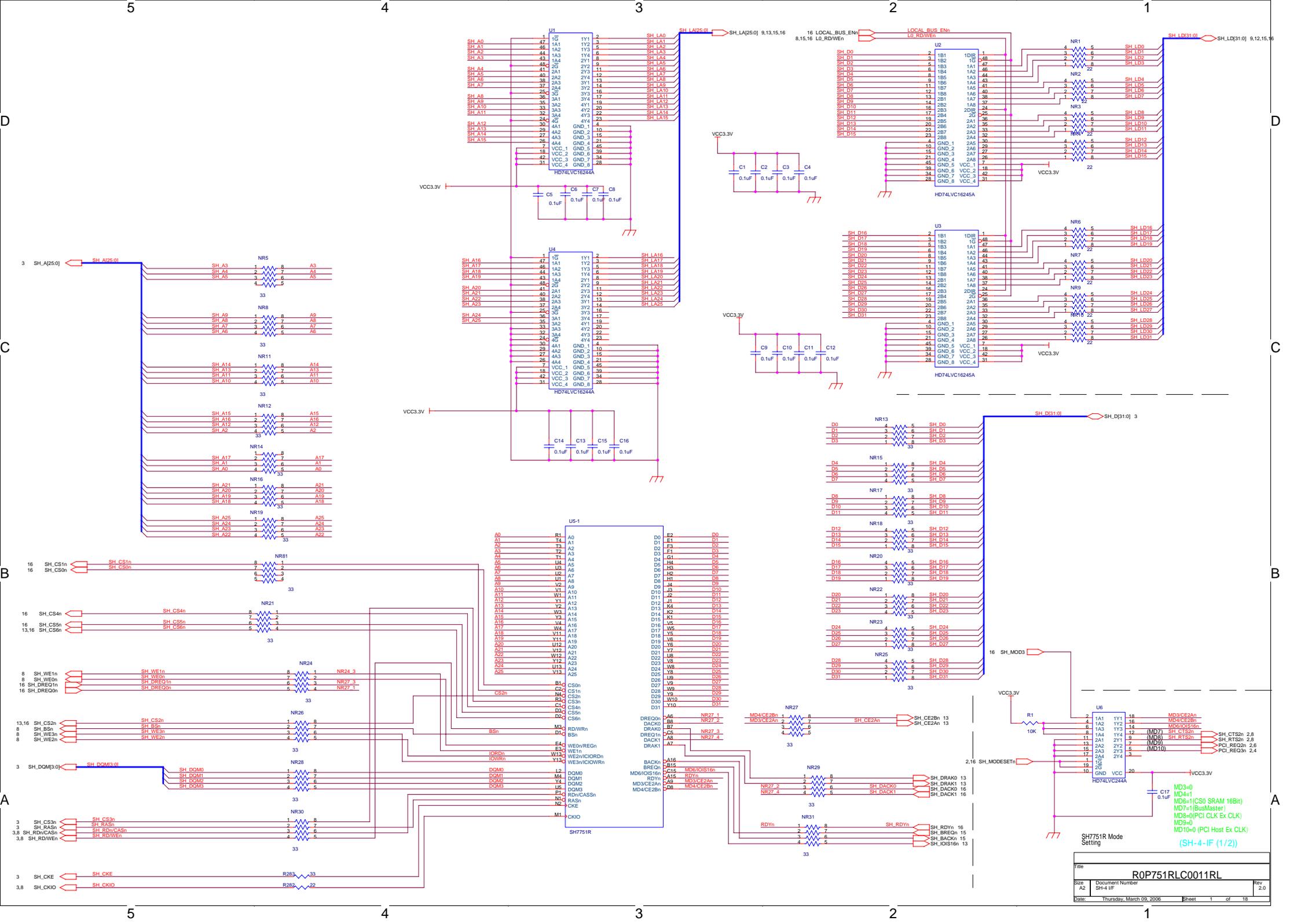
C

B

A

PPL-R0P751RLC0011RL-MAIN\_SCH

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R0P751RLC0011RL		
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A4	TITLE	1.0
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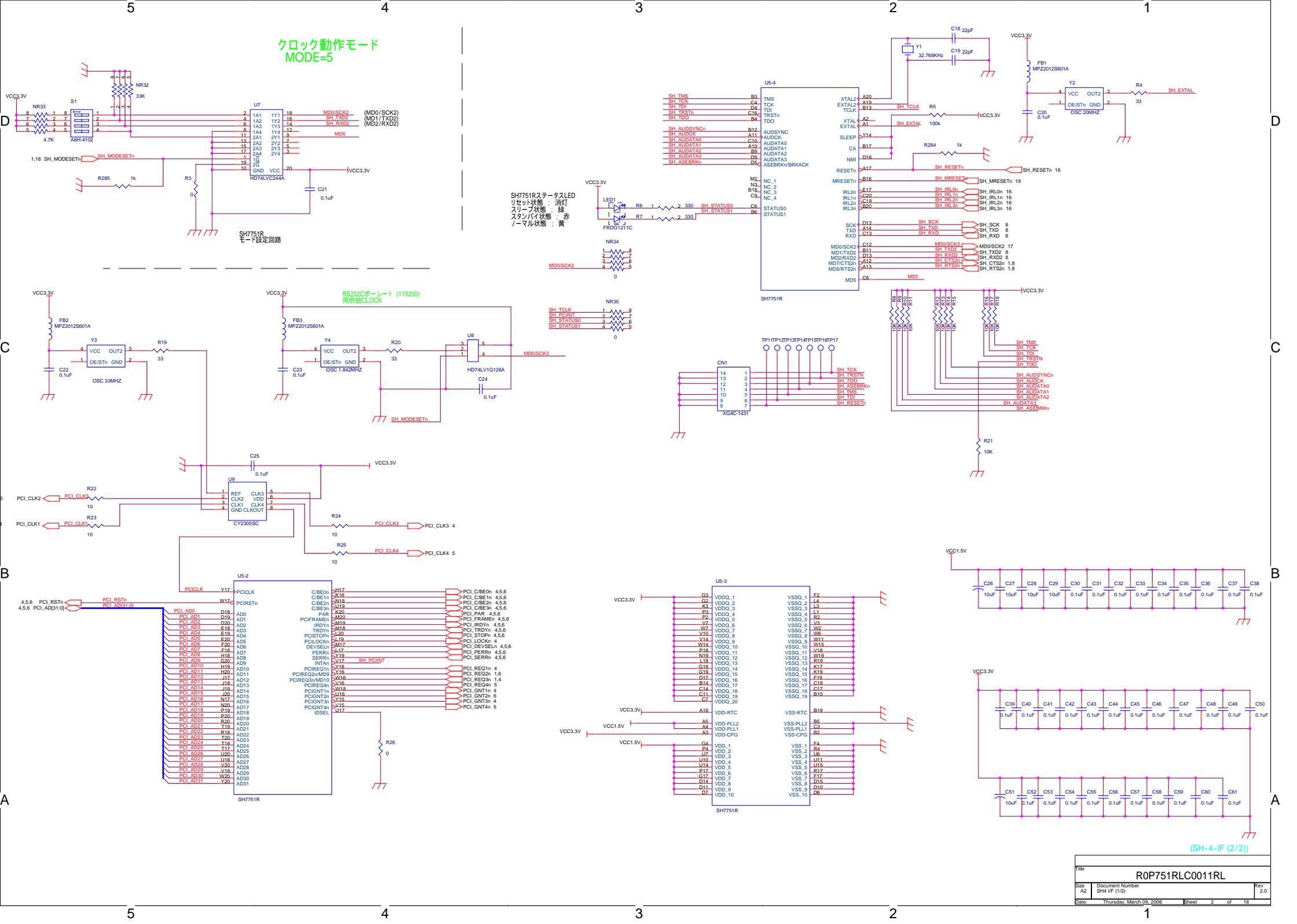


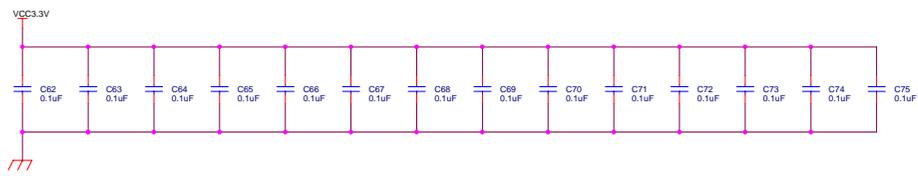
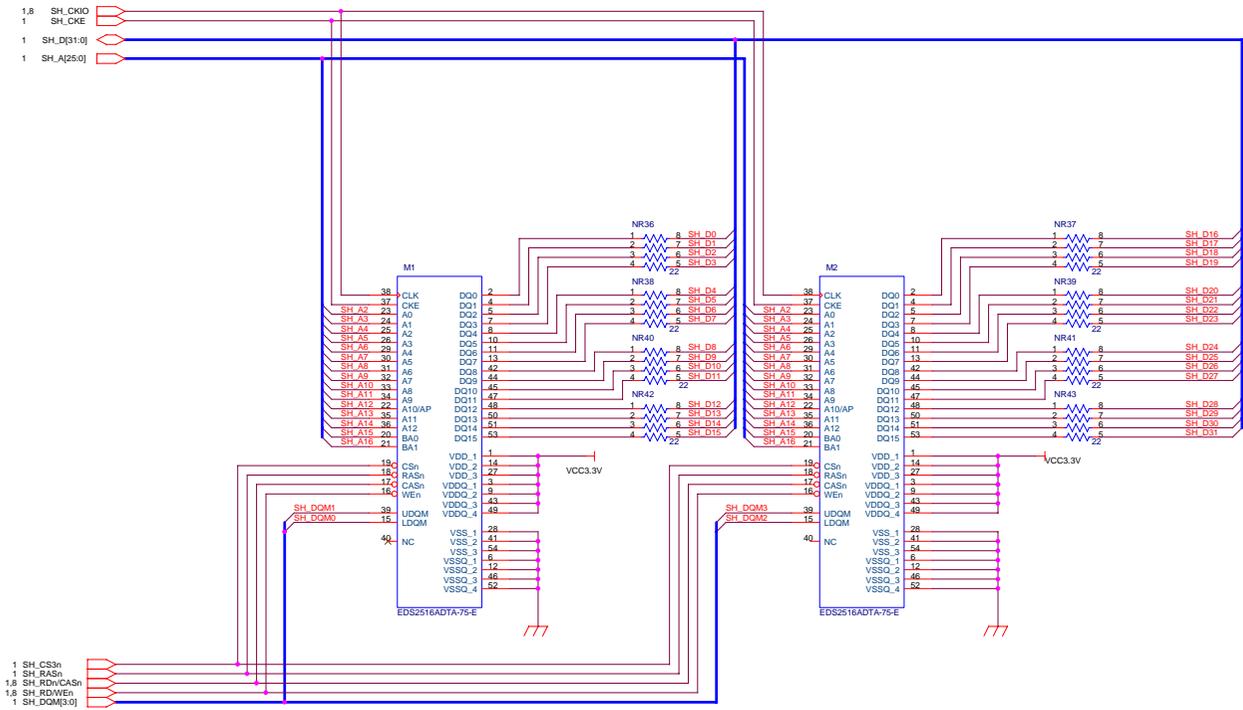
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A2	SH-4-1F	Z.0
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SH7751R Mode Setting

MD3=0  
MD4=1 (CS0 SRAM 16Bit)  
MD5=1 (BusMaster)  
MD7=1 (PCI CLK Ex CLK)  
MD8=0 (PCI CLK Ex CLK)  
MD9=0  
MD10=0 (PCI Host Ex CLK)

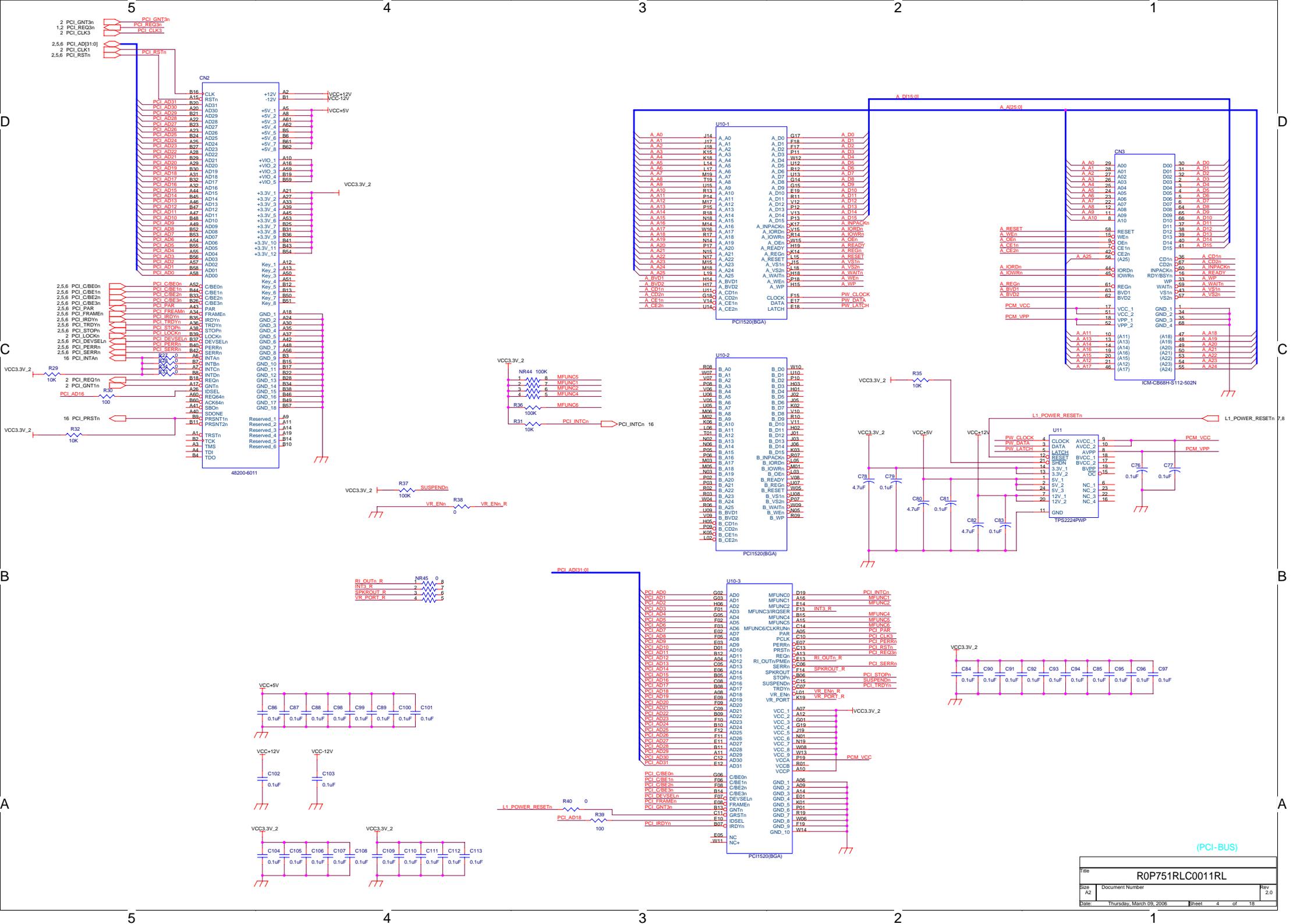
(SH-4-1F (1/2))





(MAIN-SDRAM)

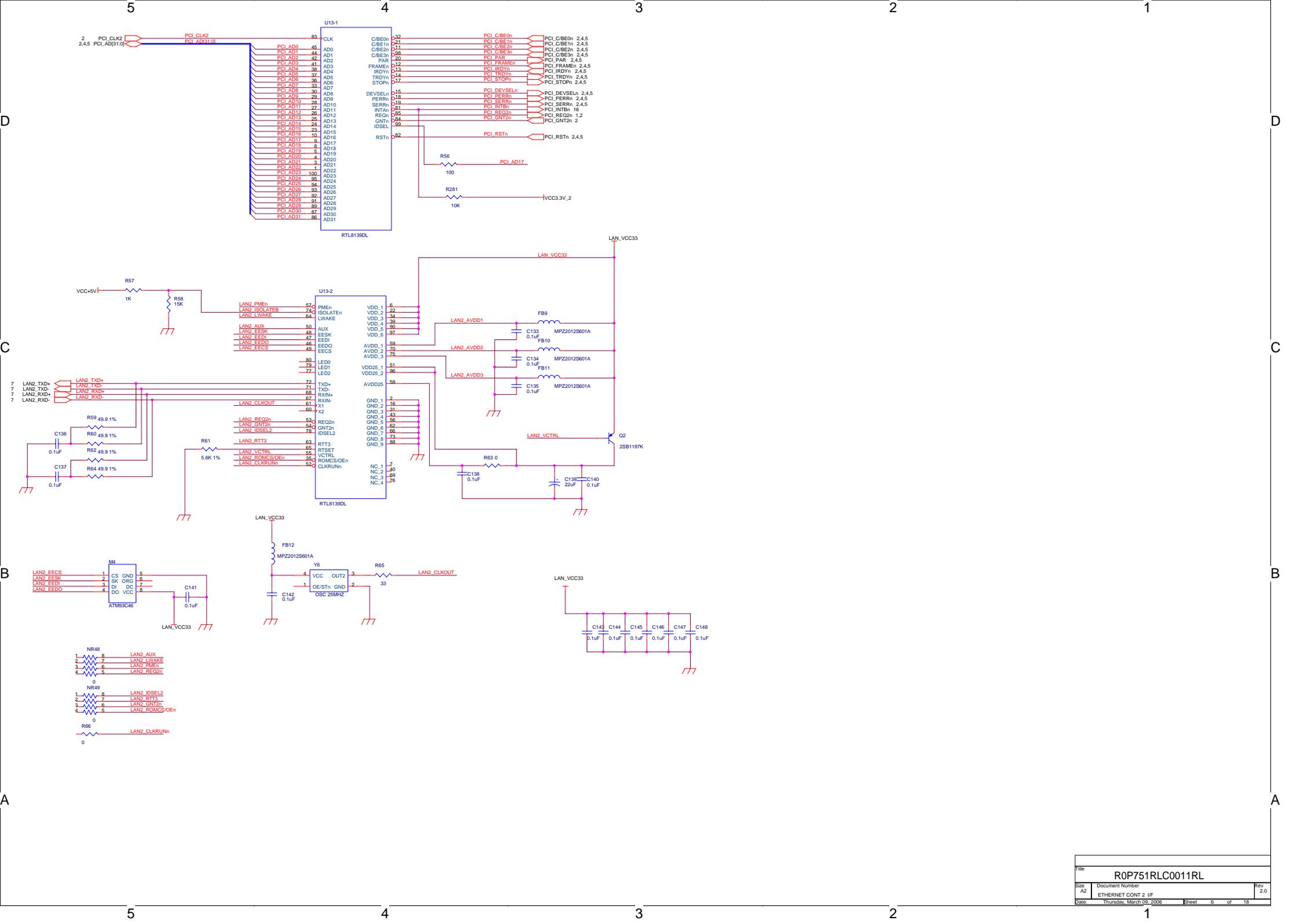
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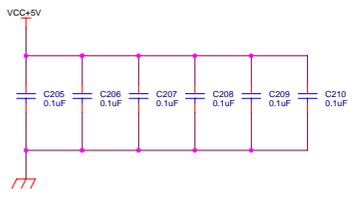
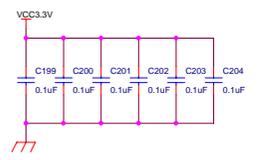
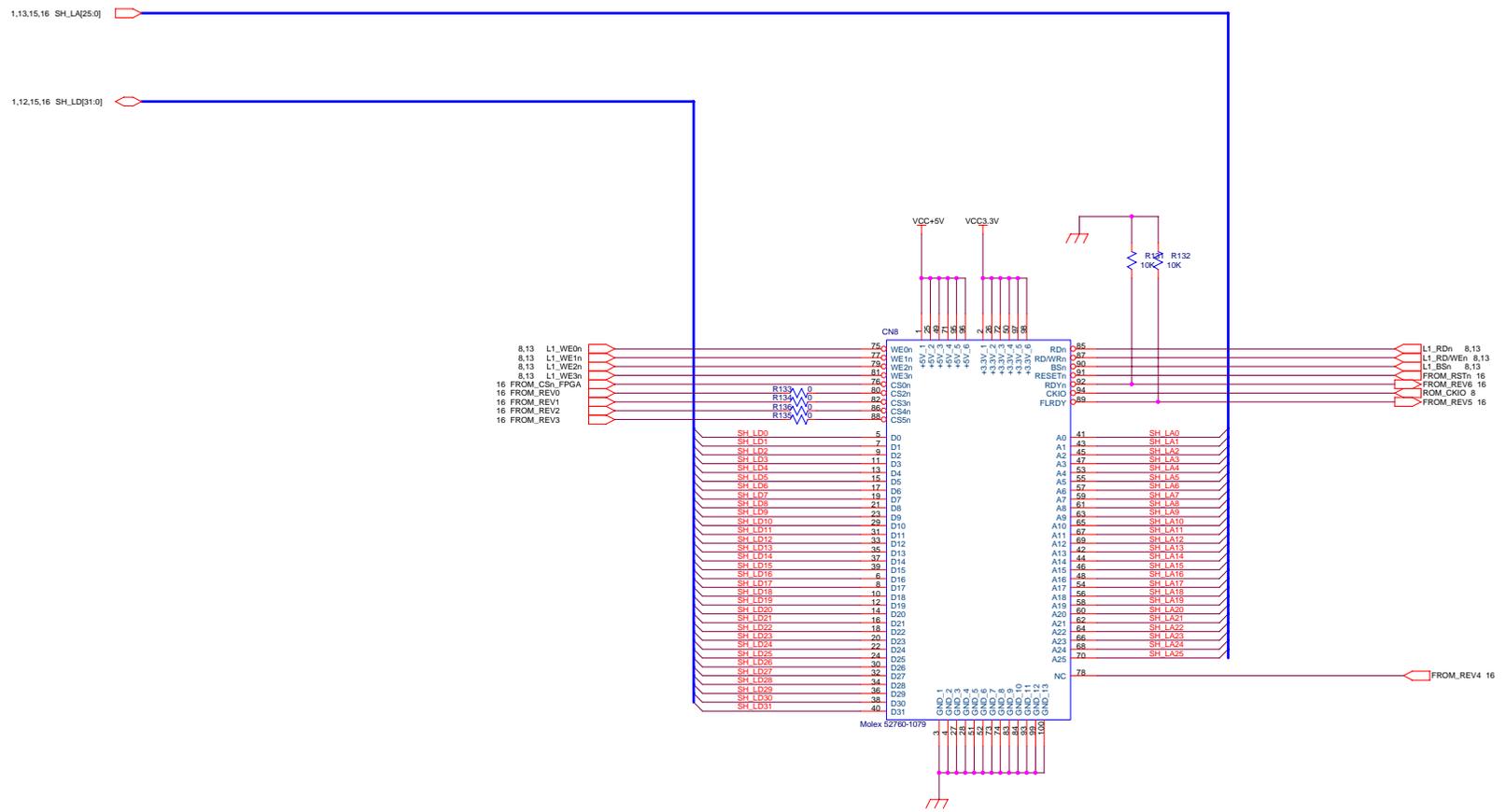
(PCI-BUS)





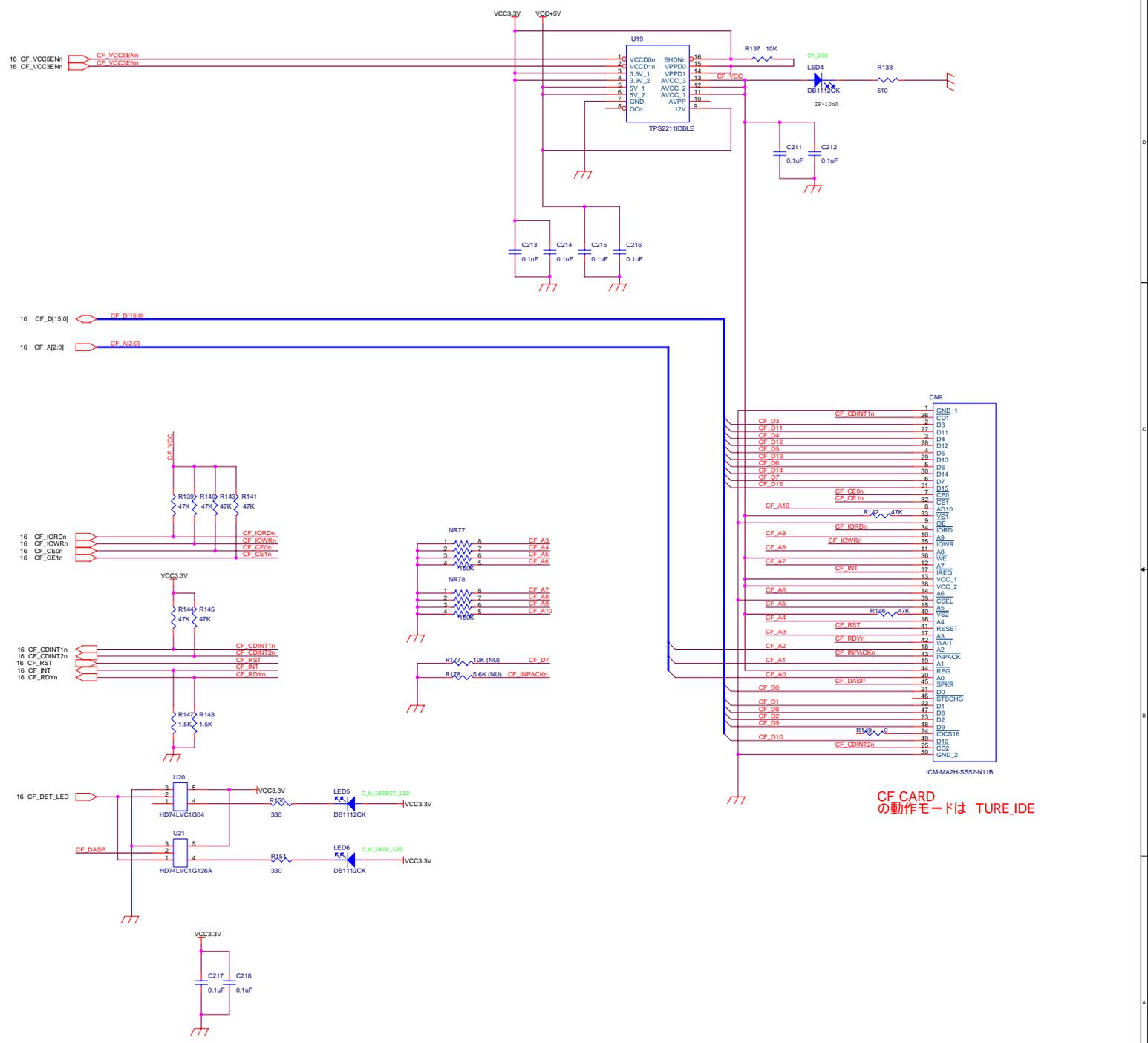






(ROM\_BOARD.IF)

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CF CARD  
の動作モードは TURE\_IDE

(CF-IF)

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A2	CF-IF	10 of 18	2.0
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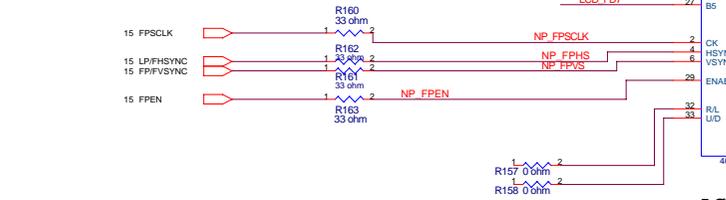
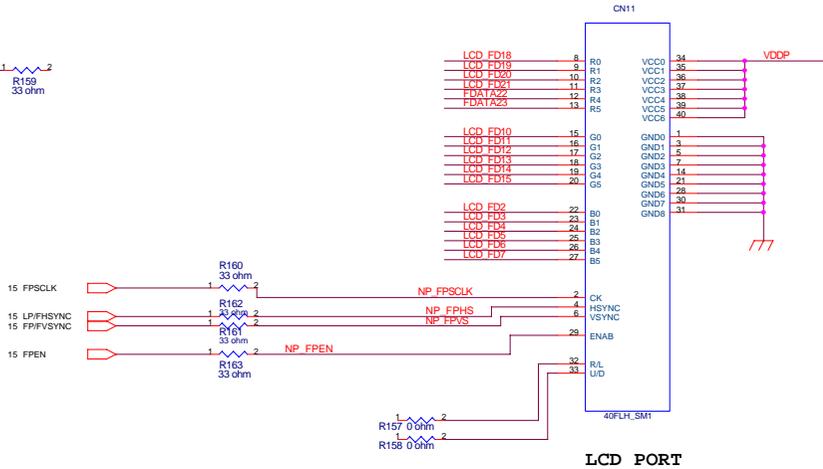
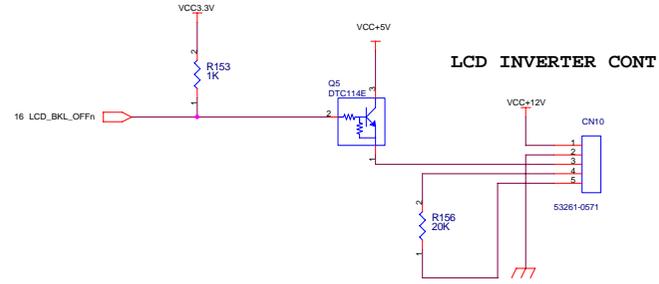
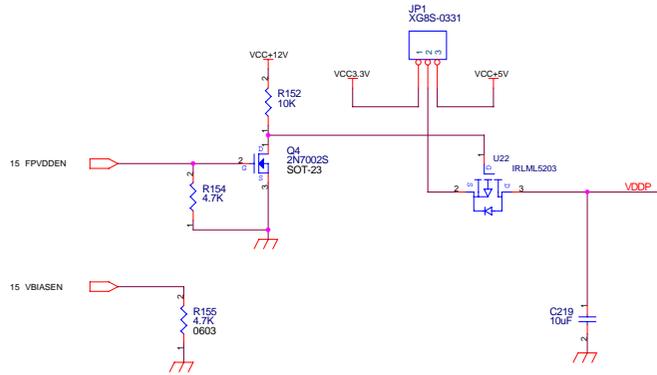
15 FDATA[23:18] FDATA23:18 RED  
 15 FDATA[15:10] FDATA15:10 GREEN  
 15 FDATA[7:2] FDATA7:2 BLUE

NR59 0 LCD FD7  
 FDATA7 2 LCD FD6  
 FDATA8 3 LCD FD5  
 FDATA4 4 LCD FD4

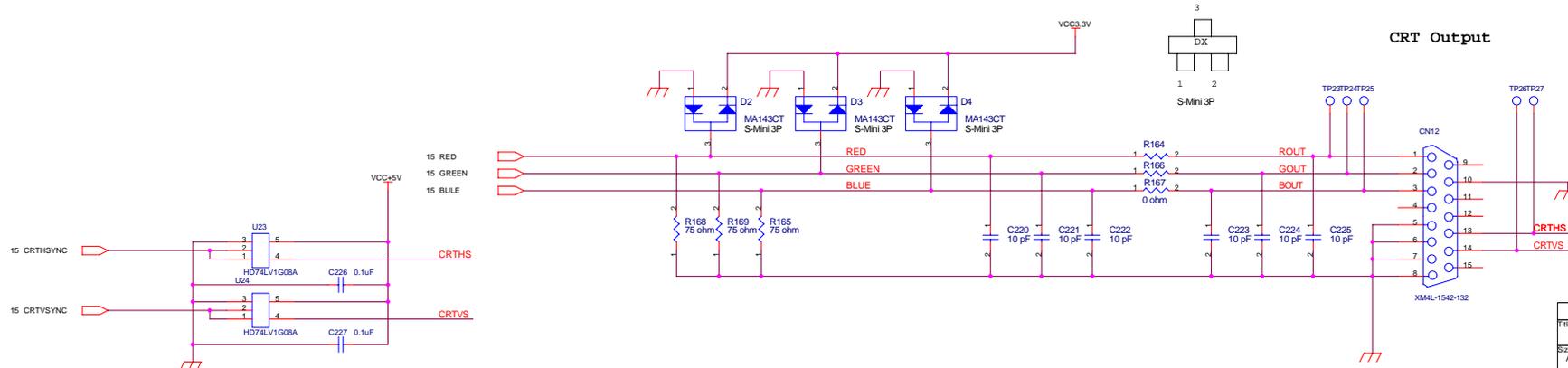
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 FDATA11 3 LCD FD11  
 FDATA10 4 LCD FD10

NR61 0 LCD FD9  
 FDATA9 2 LCD FD8  
 FDATA15 3 LCD FD15  
 FDATA14 4 LCD FD14

NR62 0 LCD FD21  
 FDATA21 1 LCD FD20  
 FDATA19 3 LCD FD19  
 FDATA16 4 LCD FD18



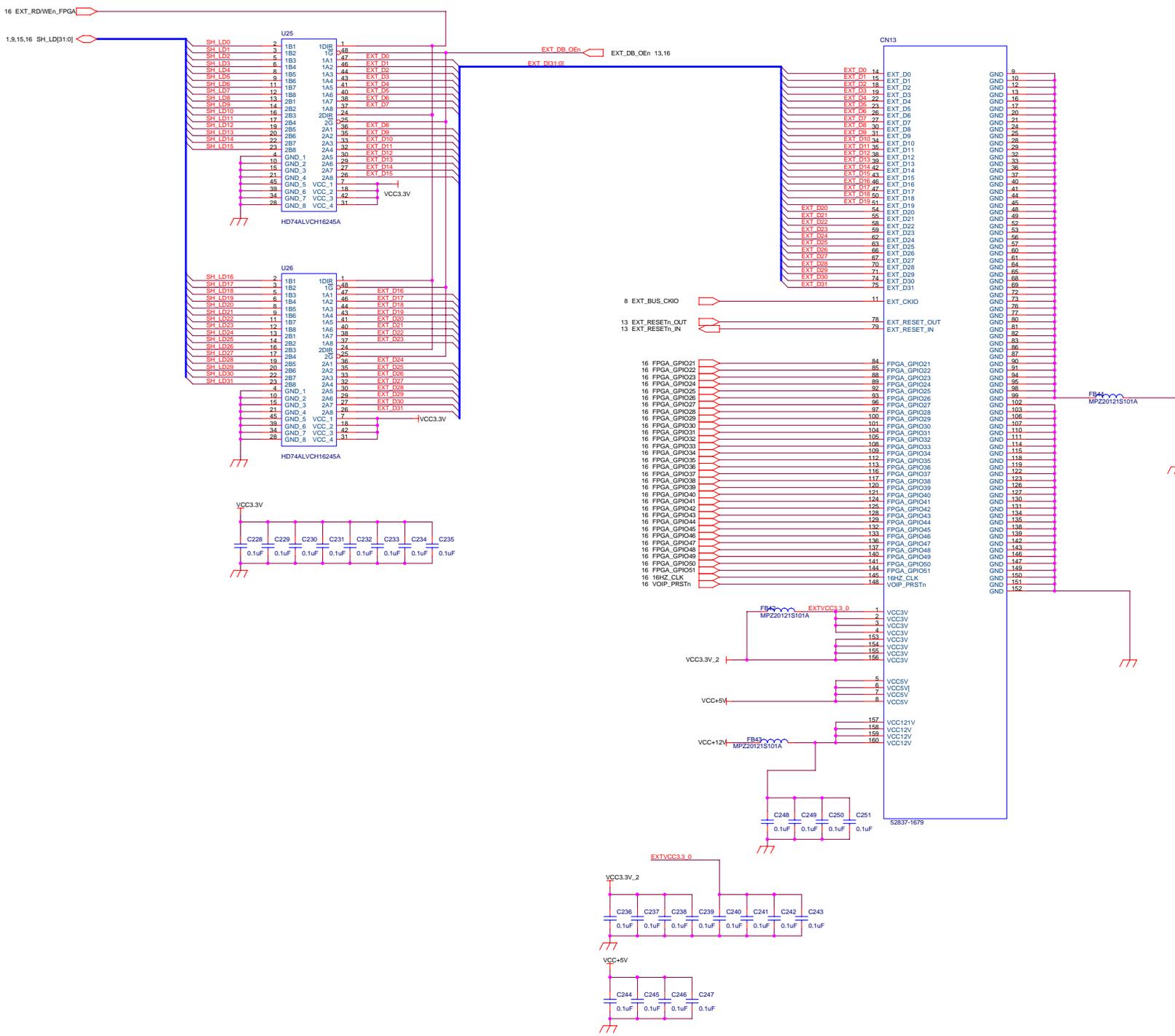
LCD PORT



CRT Output

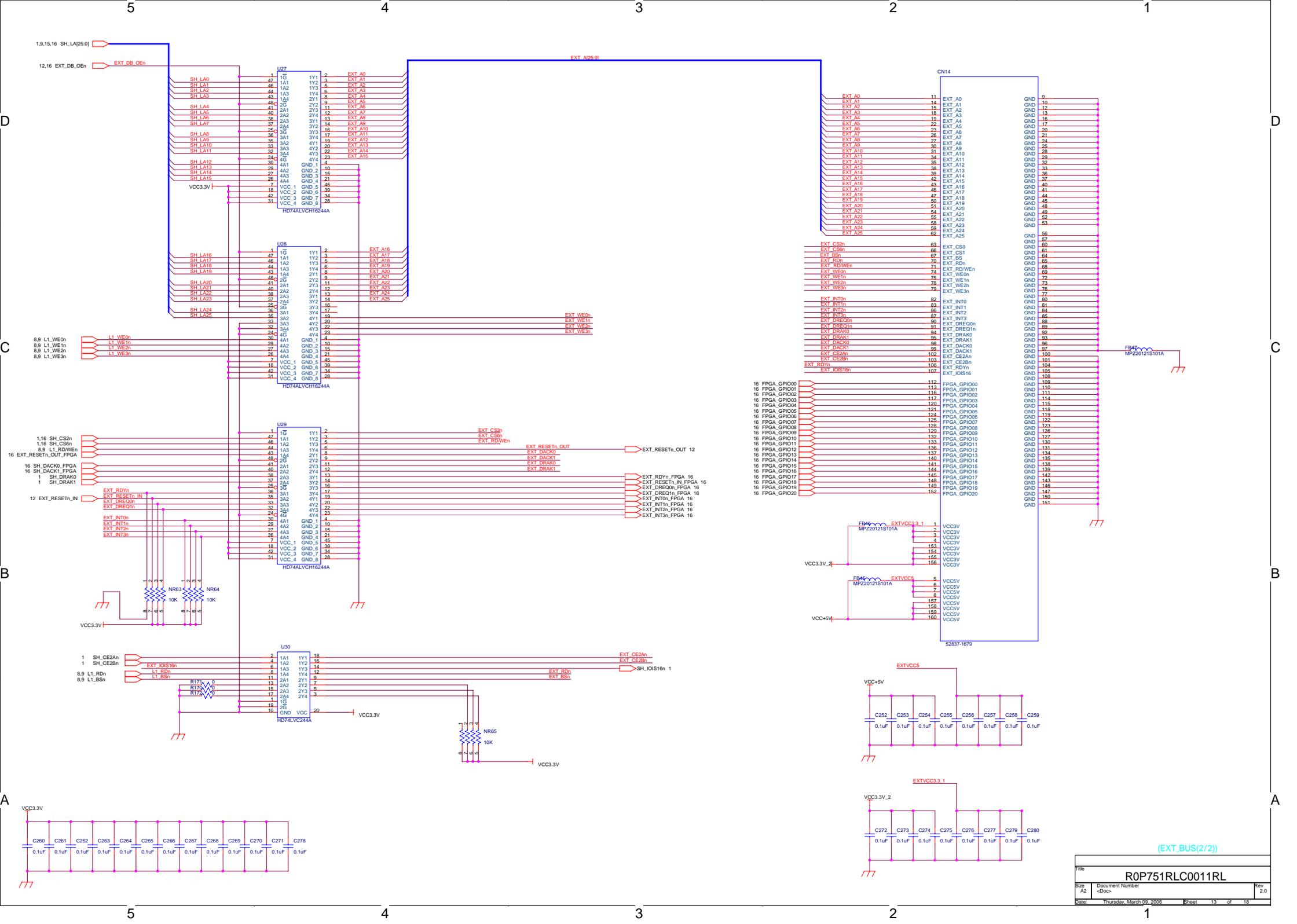
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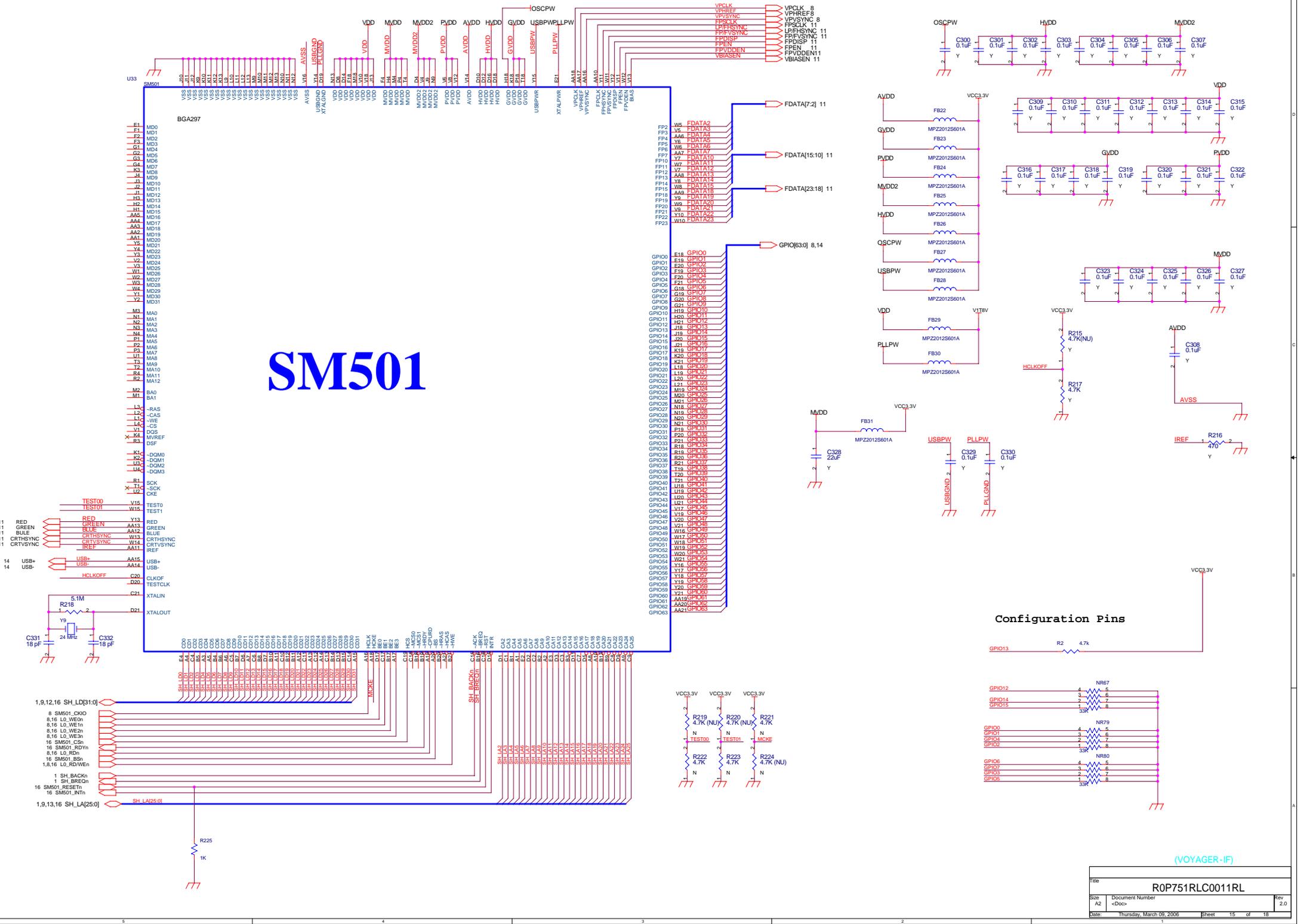
(EXT\_BUS\_1/2)

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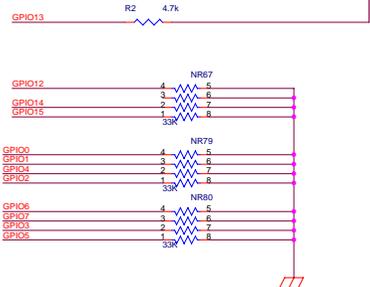


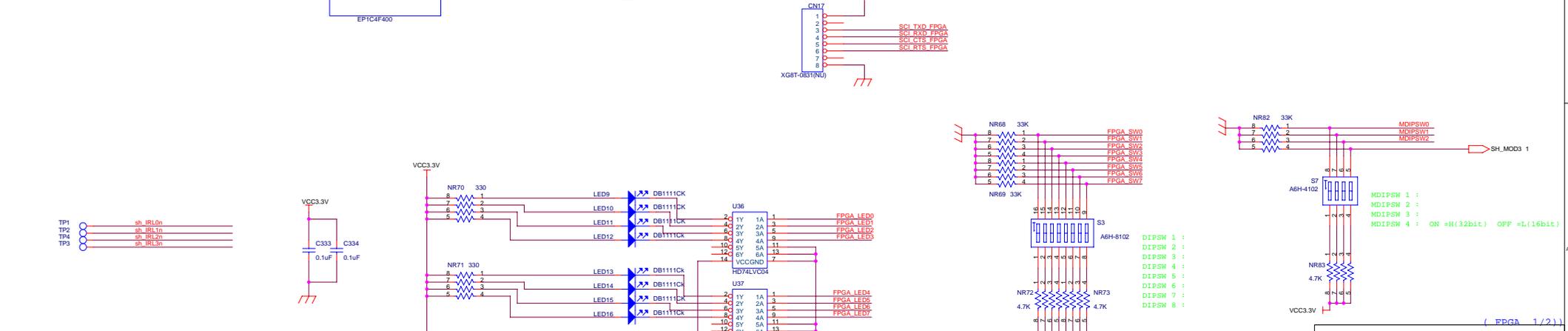
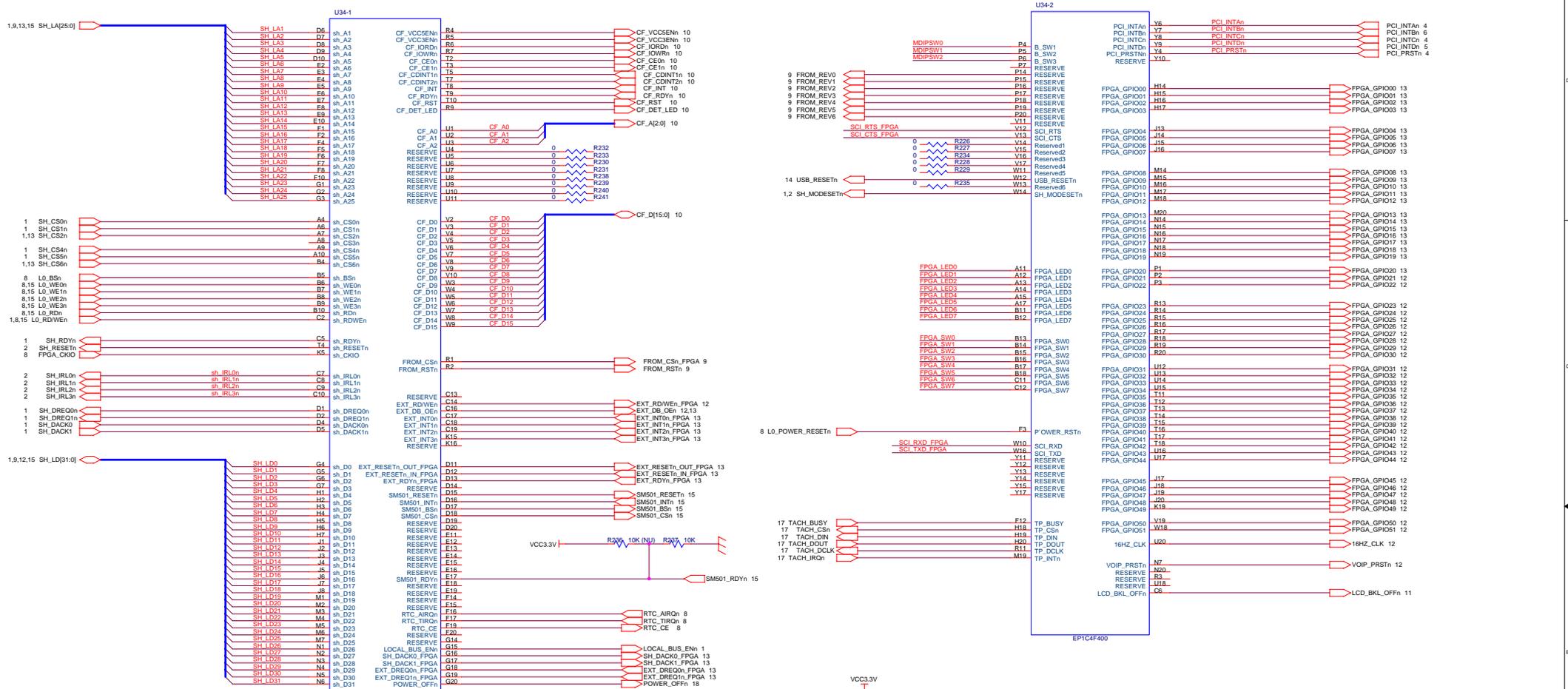


# SM501

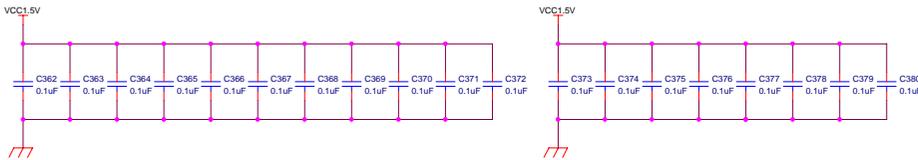
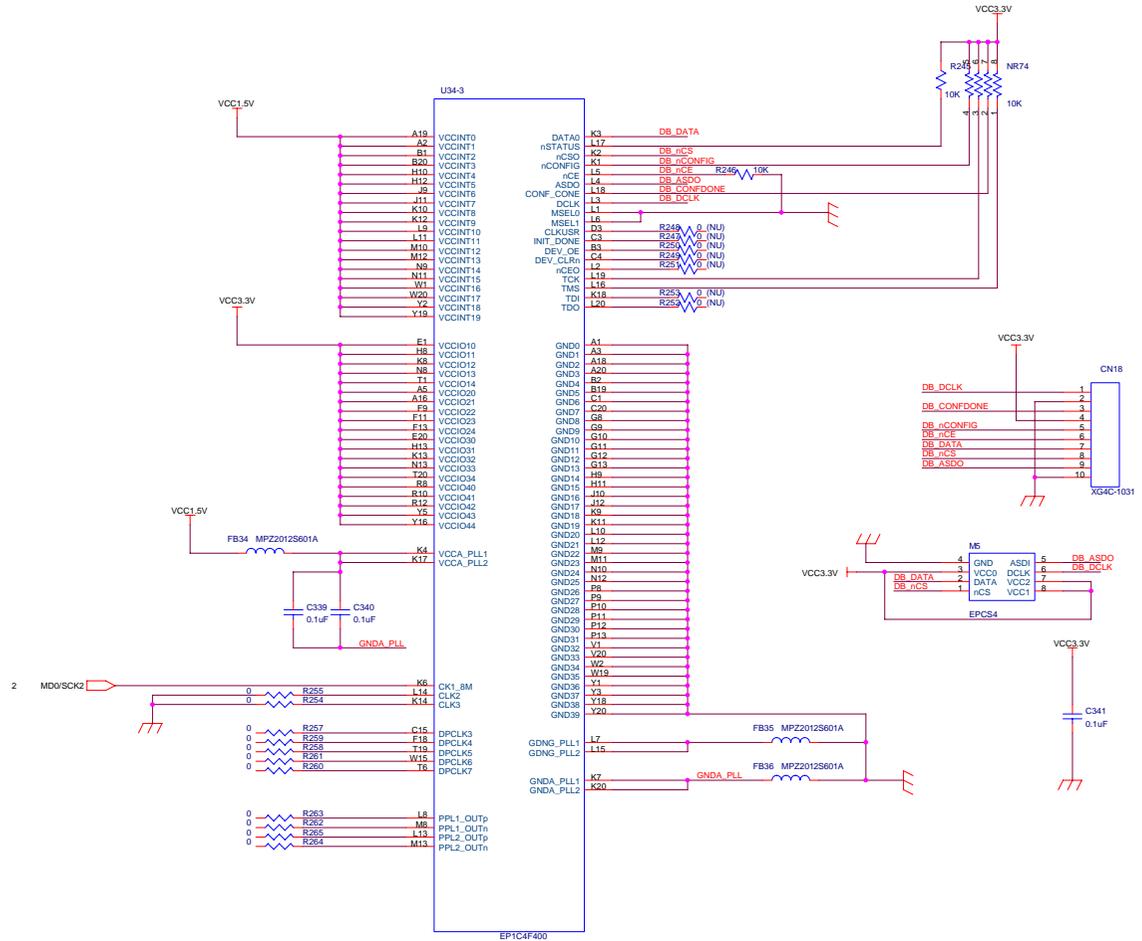
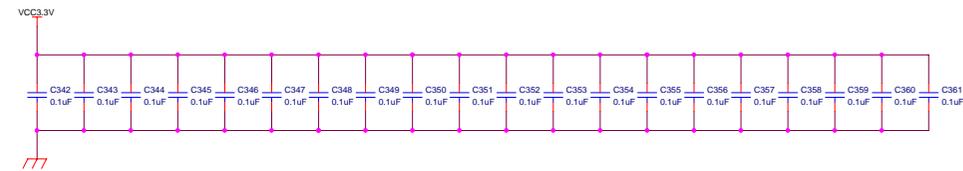
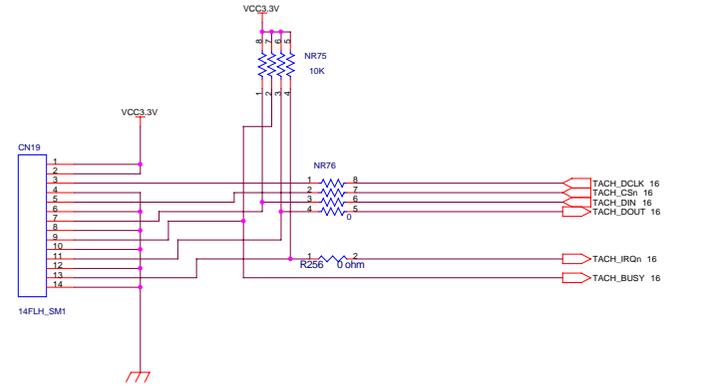


## Configuration Pins



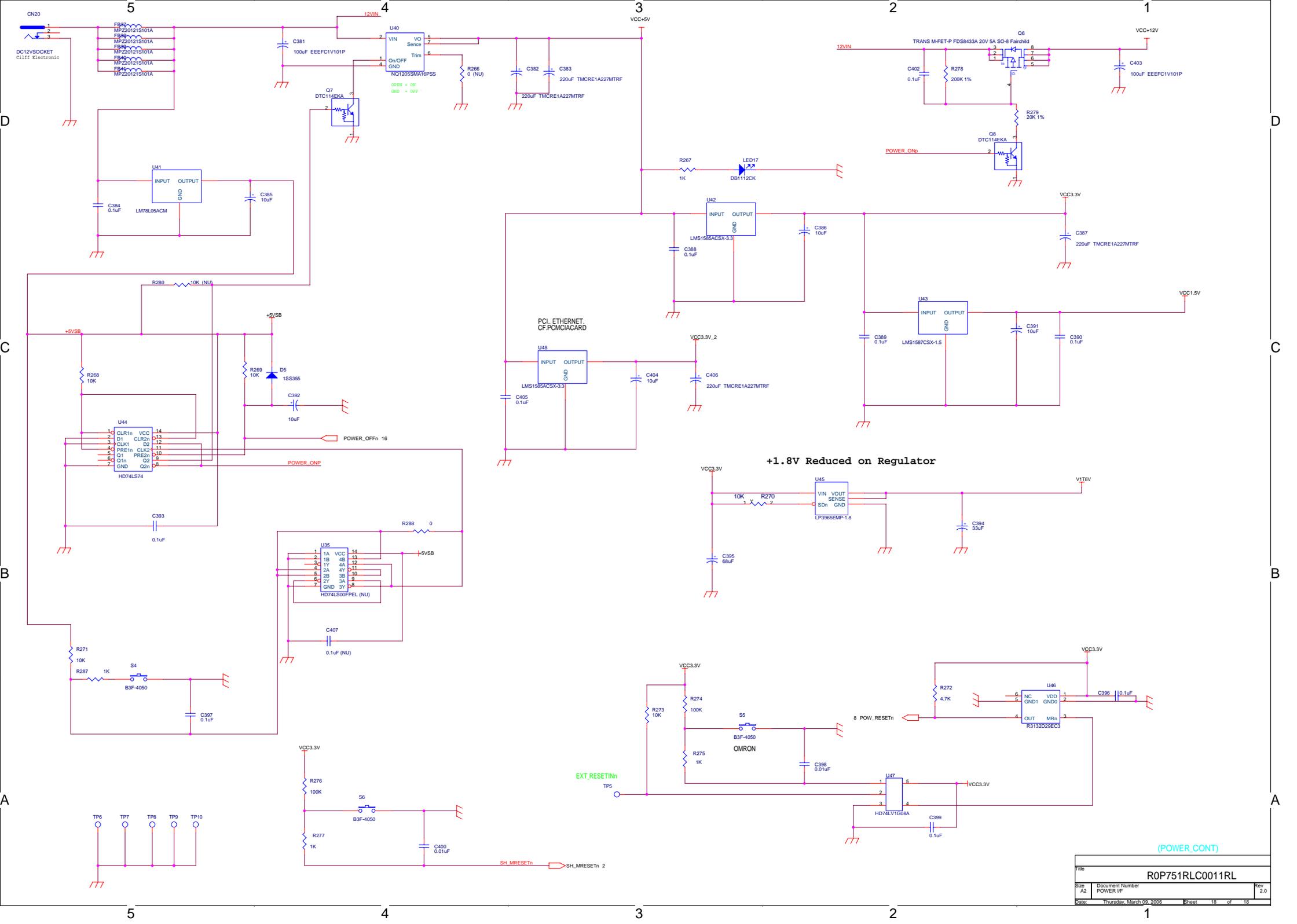


TOUCH PANEL IF



(FPGA (2/2))

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(POWER\_CONT)

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